

The Acterna FST-P 310 TestPad (\$40) all-in-one testing solution that performs SONET, DH, Carrier, data and circuit switched testing to maet the varied testing yequirements of mobile technicians. Its modular of intecture protects to sterm investment by providing the flexibility, call bility, and upgradeability (\$40) port evolving test needs.

Nosetests performed using the FST-2310 can be done in only a few autton pushes, without the need to sift through hidden setup menus. Additionally, because technicians need only minimal training to use the FST-2310, testing objectives are addressed quickly and the cost of ownership is significantly reduced.

### Highlights

- SONET/T-carrier transmission testing from DS1 to OC-48 rates
- SDH transmission testing at STM-16, STM-4 and STM-1 rates
- Circuit-switched testing capabilities including GR-303, ISDN, signaling, voice frequency (VF), and digital data service (DDS)
- ATM testing capabilities including CBR/VBR traffic generation, QoS measurements, 0.191 test cell generation, and support for OAM alarm and loopback cells
- Engineered for mobile technicians with rugged construction, lightweight design, and battery-powered operation
- Process oriented testing features such as test scripting and remote control operation



### Key features of the FXT-2310 include:

#### Application-driven icons

Test application buttons are labeled with icons that clearly depict the way the test is performed on the circuit; for example, select the "MON/THRU" icon when performing circuit-monitoring tests – select the "TERM" icon when performing circuit-terminating tests. The icons and quick setup buttons enable technicians to use the module effectively with very little training (figure 1).

# Simultaneous results analysis for different signal rates

Identify the source of problems quickly by using the FST-2310's dual results window to analyze results from different signal rates simultaneously.

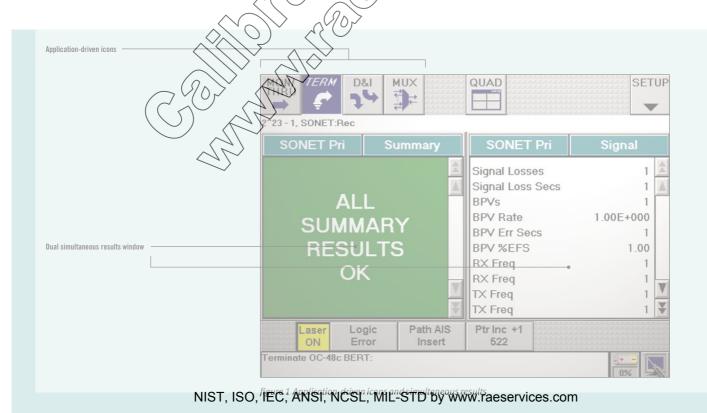
Simultaneous results analysis can also be performed on different signal results (for example, level and bit errors) allowing for easy results correlation and quick problem identification.

#### Autoconfiguration

Support for autoconfiguration is provided for all key applications.

Autoconfiguration is capable of setting different test parameters, such as framing and pattern, as well as detecting the signal's multiplexed composition. This enables users to reduce the time required for test setup. Additionally, the FST-2310 has the ability to save and store up to Massacconfigurations that can be recalled for future use.

Remotely configure tests and analyze results in detail using the remote control cull option. This allows technicians to convenient to operate the module from a remote location using the FST-2710's graphical user interface control GUI feature enables technicians to connect to the FST-2310 through the PCMCIA serial or modem card to perform tests and collect test results.



#### Scripting and automation

Reduce setup time and increase efficiency of tests by running preprogrammed scripts from a PCMCIA card. The DS1/DS3/SONET script libraries supported on the FST-2310 enable technicians to perform tests quickly and verify test results against specific criteria for a simple PASS/FAIL result. Scripts can also be customized to meet the methods and procedures of any service provider (figure 2).

#### VT100 emulation

Perform VT100 terminal emulation to connect to network elements to configure NE parameters and monitor available statistics (figure 3).

#### Timed prints and error logs

Print results every few hours, at the end of a test, or at the occurrence of an error using the FST-2310's print features.

#### Through mode for all rates

Gain access to test circuits (DS1 through OC-48) even when no test access point is provided. The FST-2310's throughmode capability monitors test circuits by unobtrusively channeling network traffic through the test instrument.

## Physical, active port, and laser active LEDs

A bright array of physical LEDs on the front panel summarizes results and clearly identifies errors detected during a test. Active port LEDs on the top panel indicate the interface(s) to use for specific tests – thus eliminating user error when connecting the FST-2310 to the network. Laser-active LEDs indicate when the transmit laser is active and when laser pulses are received.



figure 2 Use automation scripts to verify network performance with the touch of a sinale button

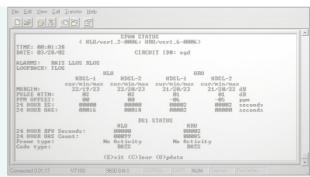


figure 3 Perform VT100 emulation to configure network elements and

### **Applications**

#### **SONET** analysis

The FST-2310 can be used to verify end-to-end network performance via BER testing at SONET rates up to OC-48 as well as to analyze SONET network performance under abnormal conditions by simulating pointer justifications and inserting error and alarm conditions. Users can also manipulate and analyze all applicable SONET overhead bytes to verify network element performance (for example, pointer adjustments, alarms, DCC bytes, SONET sync messages, and K1/K2 bytes) (figure 4).

Qualify the performance of SONET networks quickly and easily with the FST-2310's intuitive GUI. By supporting various payload mappings, the FST-2310 enables testing and verification of individual payloads inside an OC-n signal (DS1, DS3, multiplexed DS3, VT1.5, OC-n subrates).

Additional functionality includes the ability to drop and insert all mappings (OC-12/3/DS3/DS1) from an OC-48 signal. Monitor individual DS1, DS3, or OC-n subrate payloads within SONET signals for analysis, and verify signal power and signal frequency of a received signal. Technicians can also name and identify trace messages and payloads by using the FST-2310's userconfigurable path trace messages.

#### SDH analysis

The SDH functionality on the FST-2310 enables performance verification of SDH networks at the STM-16, STM-4 and STM-1 rates. The FST-2310 supports performance testing of individual payloads inside an STM-n signal (STM-16c, STM-4c, STM-1c). Ensure proper network operation with error/alarm analysis as well as the manipulation/analysis as well as the manipulation/analysis of all applicable SDH overhead bytes. Correct network timing can be morniored through pointer adjustment measurements

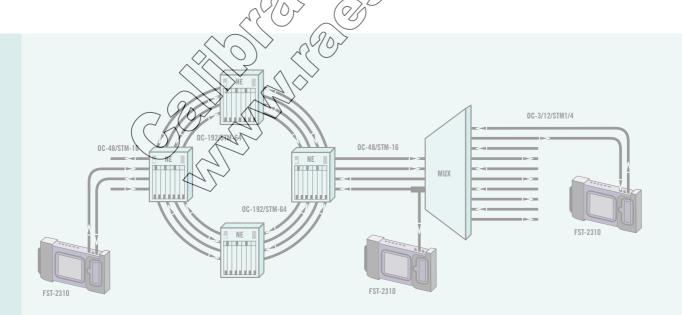


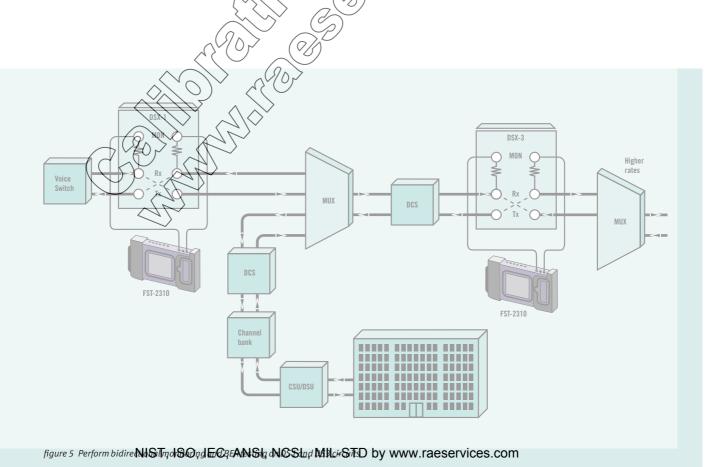
figure 4 Verify network performance al MISTs, pISOgf IEO/FAINSIet NGSL, MIL-STD by www.raeservices.com

#### DS3 analysis

Ensure DS3 network performance by performing end-to-end BER testing and measuring frequency and signal levels on the circuit under test. Technicians can qualify networks for accurate multiplexed operation by performing BER testing on one or all DS1 channels transmitted by a DS3 multiplexer. Access to the DS3 signal is provided from the DS3 interface or a DS3 signal embedded in an STS-1, OC-3, OC-12, or OC-48 circuit. Reduce total testing time on DS3 circuits by using the dual DS3 receivers to perform bidirectional monitoring, allowing for quick isolation of problems (figure 5).

#### DS1 analysis

Verify T1 network performance with the FST-2310's integrated BER, signal, alarm, and timing tests. A wide range of stress test patterns, combined with bidirectional monitoring, enables technicians to identify and sectionalize circuit problems and quickly qualify circuits for service acceptance. Users can perform VF analysis of voice trunks with the FST-2310's support of VF levels and tones measurements. The drop-andinsert test feature qualifies a DS0 channel while the T1 circuit remains service. The FST-2310 isolates of timing errors by using an ex bits clock input to ident synchronization proble interface or from the DS1 signa embedded in a



#### ATM analysis

Analyze ATM circuits at OC-12c, OC-3c, DS3, and DS1 rates. Generate multiple cell streams to perform key quality of service measurements (cell delay variation, cell error ratio, cell loss ratio, round-trip delay), cell statistics and bandwidth utilization. The FST-2310 supports OAM alarm (VP/VC AIS/RDI) and loopback cells as well as modification of the VPI/VCI, PTI, CLP, and GFC fields of the cell header. Also supported are the 0.191 ITU standard test cell and the ability to save up to four transmit profiles (figure 6).

GR-303 protocol link analysis
Perform basic GR-303 protocol link
analysis in Monitor/Through mode at
the 64-K data rate on a call processing
(TMC/CSC) or operations channel (EOC).

Analysis is conducted unobtrusively to decode messages on the system's protocol links. Technicians can monitor call statistics, filter call control messages based on cause code, or monitor layer-2 packet statistics to qualify proper functioning of the GR-303 interface. The GR-303 option also provides support for tracing calls based on filter criteria (for example call reference value – CRV) established through the test setup. Once the call is traced, clicking on the appropriate call provides decodes of the protocol message that were exchanged to establish the Call.

#### **ISDN PRI testing**

Emulate an ISDN TE device, such as a PBX, to place and receive voice and data calls on an ISDN PRI circuit. The Acterna FST-2310 TestPad enables users to monitor and capture signaling messages exchanged on the D-channel. Caller ID/Caller ID blocking, 21-digit dialing and DTMF digits are all supported. Call status results and progress reports provide an additional level of detail to ensure calls are successfully established (figure).

The IST 23 10'S ISDN furctionality supports of &T specificall control (PSS), DMS-10, National ISDN-II). The IST-2310 can be used for backup D channel testing to switch between standby and in-service D-channels, bidirent and monitoring with full exchanged on the D-channel, as well as testing of multiple DS1 lines with NFAS testing.

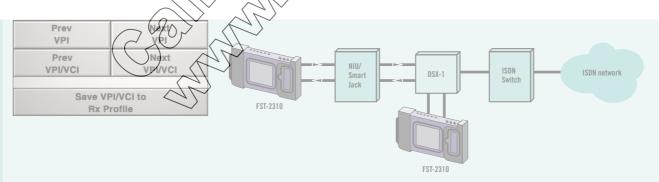


figure 6 Search for ATM traffic on DS1, DS3, OC-3, or OC-12 circuits

 $figure~7~\textit{Perform ISDN terminal emulation as well as monitor ISDN links from DS1, DS3 and STS-1 circuits$ 

#### Signaling analysis

Emulate the PBX/switch for placing and receiving calls and monitor signaling digits for detailed analysis. The FST-2310 signaling features include E&M, loop start, and ground start support for placing and receiving calls. The FST-2310 provides support for DP, DTMF, and MF digit recognition. It can also perform interevent and interdigit delay measurements as well as event and digit duration measurements.

The signaling (ABCD) bits for all DSOs can be displayed in one simple result window. A speaker, microphone, and handset are provided for verifying voice integrity.

#### **Loopback applications**

Sectionalize T1/T3 circuits with the FST-2310 and its support for NIU/CSU loopback and user-programmable loop codes. The FST-2310 can sectionalize the network and verify the functionality of intelligent line-equipment and repeaters with its ability to loopback T1/T3 network elements (figure 8).

#### Online help

The easy-to-use online help screens provide information on product descriptions, common applications, product specifications, cabling diagrams, and contact information (figure 9).

#### Software support

All Acterna products are continually being updated through software enhancements. The Acterna FST-2310 TestPad, in particular, keeps pace with advancing technologies through enhanced software revisions to add value to its users and customers. Software upgrades are done in the field in minutes through the PCMCIA card in reface.

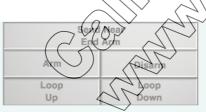


figure 8 Send loop codes to a T1 NIU/CSU

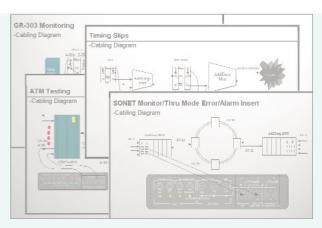
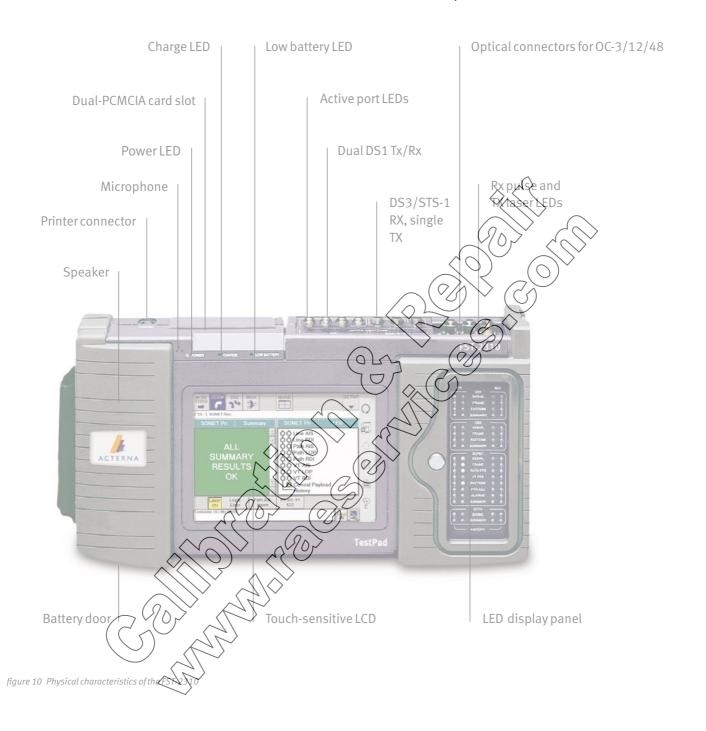


figure 9 Online help provides quick cards and cabling diagrams



Overall dimensions 7.5 x 11.5 x 2.25 in (19 x 29 2 x 5.7 cm)  Weight 5 lb (2.27 kg), with battery Environment  Environment  Environment  Environment  Environment  Environment  Storage 4*F to 113*F (0°C to 45°C) Shock and vibration  Meets IEEE-743  Electrical  Battery type 10.8 V Nickel-Metal-Hydride (NiMH) Operating time  Up to 1.5 hours for performing DS3/DS1 tests  Recharging period  Maximum 1.5 hours for performing DS3/DS1 tests  Recharging period  Maximum 1.5 hours for Performing DS3/DS1 tests  Recharging period  Movernor 19 VDC, 2.6 amps 90-240 VAC, 45-65 Hz  Uptical specifications for 0C-48  Optical connectors  10 C-48 1350 nm High-Power Transmit – FC, SC, or ST 10 C-48 1350 nm High-Power Transmit – FC, SC, or ST 10 C-48 1550 nm H	
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Rx clock frequency +3% or ±3 mA from 61 mA to 175 m	A
Rx clock frequency  As dBm to 28 dBm	
Receiver shutdown  -6 dB or higher  -6 dB or higher  -6 dB or higher  -6 dB or higher  Exceeds TR-TSY-000499  Output specifications for DS1	
Connector type	Bantam jack
LBO level	
Line build-out of 0, -7.5, -15.0, and	00 E 4D - f
cable loss at 772 Hz	–22.5 dB of
LBO tolerance	–22.5 dB of
±2 dB for -22.5 at 772 kHz ±1 dB for 0, -7.5, and-15 at 772 kH	–22.5 dB of
Line codes	

Error insert type Logic, BPV, or Frame

Per applicable specifications

Pulse shape

#### Ordering information Sources Analyzer Options Specifications/recommendations used User Interface Module Advanced stress patterns TB2310-ASP ATM analysis for OC12c TB2310-ATM-0C12 TestPad 2000 (includes soft ATM analysis for OC3c TB2310-ATM-OC3 ITU-T recommendation G.703 carrying case, kickstand, ATM analysis for DS3 TB2310-ATM-DS3 AT&T publications CB113, CB119, CB132, CB143 AC adapter/charger, printer cable) ATM analysis for DS3 and DS1 TB2310-ATM-DSN ANSI T1.403-1995 FST-2310 Application Modules Digital data services (DDS) analysis TB2310-DDS AT&T publications PUB62508, PUB62411 Secondary receiver for DS3 and STS-1 interfaces ITU-T recommendation G.824 DS1 Communications Analyzer TB2310-DS1 TB2310-DUALRX TR-TSY-000499, category 1.2 DS3/DS1 Communications Analyzer TB2310-DS3 Remote control GUI TB2310-REM ANSI T1.102-1993 STS-1/DS3/DS1 TB2310-STS1 SDH testing at STM-16, STM-4, and STM-1 Bellcore GR253-Core-1995 Communications Analyzer TB2310-SDH Bell Pub 62411-1990 OC3/3c, STS-1/DS3/DS1 TB2310-0C3XX\* TB2310-ELE Communications Analyzer TB2310-0PT SONET/DS3/DS OC12/12c, OC3/3c, TB2310-0C12XX\* Fractional T STS-1/DS3/DS1 GR-303 analys ¶B23≥0-GR303 Communications Analyzer TB2310-ILE OC48/48c, OC12/12c, TB2310-0C48XX\* TB2310-PRI OC3/3c, STS-1/DS3/DS1 TB2310-SIG Communications Analyzer TB2310-TIM OC48/48c, OC12/12c, TB2310-0C48DXX TB2310-VT100 OC3/3c, STS-1/DS3/DS1 Communications Analyzer with 1310 nm and 1550 nm laser AC-31705 AC-31891 \*Specify type of optical connector BA-014081 ement battery ng case, large, soft CC-44605 Garrying case, multimodule, soft CC-451-58 Carrying case, hard CC-45458 Rack mount for TestPad RM-TTC2000

Hardware packages										
	DS1	DS3	STS-1	0C3	OC12	0C48	OC48D	UIM	VT100	Dual Rx
TB2310-P1	Χ	Χ	Χ					Χ	Χ	Χ
TB2310-P2	Χ	Χ	Χ	X				Χ	Χ	Χ
TB2310-P3	Χ	Χ	Χ	Χ	Χ			Χ	Χ	Χ
TB2310-P4	Χ							Χ	Χ	Χ
TB2310-P5	Χ	Χ						Χ	Χ	Χ
TB2310-P6	Χ	Χ	Χ	Χ	Χ	Χ		Χ	Χ	Χ
TB2310-P7	Χ	Χ	Χ	Χ	Χ		Χ	Χ	Χ	Χ

Software packag	ges									
	FTI	TIM	SIG	ASP	ILE	DDS	PRI 🔿	OC12 ATM	OC3 ATM	DSN ATM
TB2310-SW1	Χ	Χ	Χ	Χ			$\Diamond$			
TB2310-SW2	Χ	Χ	Χ	Χ	Χ	Χ		> -		
TB2310-SW3	Χ	Χ	Χ	Χ						
TB2310-SW4	Χ	Χ	Χ	Χ	Χ	Χ	(1/0)		<b>/</b>	
TB2310-SW5						~			Χ	
TB2310-SW6							()	$\langle \langle \rangle \rangle$	Χ	Χ
							<	$\cup$		

#### Additional application modules available

FST-2510a High Speed Optical Analyzer

FST-2416 SDH Services Module

FST-2209 T1/T3 Services Module

FST-2207 T1/T3 Wireless Services Module

FST-2230 E1 Data Communications Module

FST-2109 Copper Analyzer Module

FST-2357 DSL Broadband Services Module

FST-2802 Gigabit Ethernet Services Module

BAT-2700 Base Station and Air Interface Test Module

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Actema is the world's largest provider of test and management solutions for optical transport, access and cable networks, and the second largest communications test company overall. Focused entirely on providing equipment, software, systems and services, Acterna helps customers develop, install, manufacture and maintain optical transport, access, cable, data/IP and wireless networks.



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