To receive a calibration and/or repair quote-RMA from R.AE. Services Inc. Click here>> www.raeservices.com/services/quote.htm

ATM Internetworking Analyzer

A Field Service Solution for Internetworks

Field service organizations require and analyzer that can handle the m today's complex internetwo need one that's easy to 6 use. The Domino®-360 A provides a complete solo The analyzer car all major protocols, an traffic on ATM n excellent tool for ATM netw troubleshooting. The Domina analyzer is more than an ATM instrument. Because it can link to any other Domino analyzer, it also performs as an internetworking analyzer, allowing you to track a problem across multiple segments of varying types.

The Domino-360 ATM analyzer supports automatic ATM ILMI address registration which enables 'plug and play' operation in an ATM network environment. It can emulate either an ATM user or an ATM network using ATM Forum UNI 3.0, UNI 3.1 or UNI 4.0 signaling. With its ability to monitor PNNI signaling, it can also be used

supports (yearn)c real time capture, filter and triggerize based on cell header, cell paylod, JTM addresses and MAC/IP/IPX

Nowerful transmit capabilities allow cells or immes to be imported from a capture file or constructed by an ATM cell editor or frame builder, and then transmitted onto the network. Frames are automatically segmented into cells for transmission. With ATM's unique frame builder feature not only can cells be transmitted, but they can also be easily constructed for popular internetworking protocol stacks such as TCP/IP over AAL5.

Lightweight, Modular, Portable Design

Weighing less than seven pounds, the Domino-360 ATM analyzer connects to your notebook PC for easy operation. Up to eight Domino analyzers can be linked together to perform multi-segment analysis. By generating and receiving traffic simultaneously, the instrument is an ideal tool for benchmarking routers, bridges, gateways, and other equipment prior to installation or after an upgrade.

to troubleshoot a PNNI network ISO, IEC, ANSI MIL-STD by www.raeservices.com environment. The Domino-360 ATM

For monitoring, troubleshooting, and simulation in internetworking environments

- Full-duplex Auto Channel Discovery with complete Call Statistics
 - SVC Emulation, Per channel Statistics and extensive QoS measurements
- Powerful filter and triggering capabilities
- Lightweight, modular and highly portable
- Supports OC-3/STM-1 single-mode, multimode, and UTP, E3, DS3, E1, DS1, ATM 25 Mbps
- Plug and Play modular design also supports Fast Ethernet, WAN and Frame Relay



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chassis. The DominoPLUS chassis supports a variety of LAN.

The definition from the first through the first and th WAN and ATM interface modules. Changing modules takes only seconds and requires no special tools. This design results in lower ownership costs and greater portability when troubleshooting in today's multi-topology network environments.

High Performance Architecture

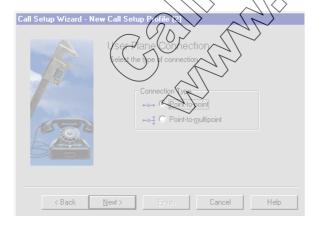
The Domino-360 ATM is capable of extremely high performance because it employs multiple, independent RISC processors, each dedicated to performing separate functions. For example, separate RISC processors capture and decode traffic. High-speed links connect these processors, thus avoiding bottlenecks between them and allowing real time network traffic analysis.

ATM Forum Compliant

The Domino-360 ATM explicitly supports emulation and monitoring in environments compliant to ATM Forum UNI 3.0 and 3.1, UNI 4.0 and PNNI 1.0 and 1.1. In addition to UNI 3.0, 3.1 and 4.0 decodes, full PNNI, Q.2931, Q.2971, LANE, SSCOP. OAM and over 1,000 additional upper-layer decodes are fully supported. Where appropriate, point-to-point or point-to-multipoint ATM calls can be placed and received. ILMI event monitoring and full automatic address registration is supported for both ATM network and end-point addresses.

Wizards Make it Easy

Domino-360 ATM has a Windows®-based graphical user interface and includes extensive use of intelligent Wizards to simplify setup tasks and other complex ATM-related feature For setting up traffic to send onto the network, the Traker Wizard intuitively walks users through the setup to lCBR, VBR and bursty data. The Call Setup Wizard wak through the entire specification of ATM Call Reg messages. To help simplify the call setup process/ @asonabi default values are provided so that a valid call call with the user defining only the called an addresses. A programmable repetiri user-defined cells or frames cap



The ATM Address Wizard makes the building of ATM address libraries guick and easy and allows symbolic naming of ATM addresses. In addition to the ATM address library, call profile configuration. An unlimited number of these configurations can be created, and the user can even create a desktop shortcut to them. Saved analyzer configuration information includes window positions, display properties, filters and address aliases (symbolic names). This is useful where different individuals must use the same test equipment in multiple network configurations.

Filter and Triggering Capabilities

Filter and Triggering capabilities support filtering and triggering of specific ATM call data without prior VCC knowledge. The application monitors the ATM signaling channel and finds user-specified and calling party ATM addresses in the call request mess Once identified, the filters or trigger (or both) are ically configured to filter/trigger on the resulting s especially useful for troubleshooting and environments. Dynamic gering eliminates the need to search of captured data in the hopes of finding th of interest. The Domino-360/ATIV apture buffer size of 16 griser setting, wraps or stops Mbytes, wh when full.

to the addresses where the 70mno-360 ATM also supports real time based on ATM and MAC/IP/IPX K determines which VCCs carry the specified pures data and reassembles AAL5 frames. The 660 ATM supports several types of filtering that an pattern matcher to filter or trigger over a range of add sees or data. The trigger location is displayed in the Varie/cell decode

hile employing the powerful hardware-based filter and triggering capabilities for full line rate performance, the application supports an unlimited library of filter profiles. With this library the user can define as many filter/trigger criteria as desired over time, and simply turn them on or off as needed.

Extensive Statistics

Extensive SVC and PVC statistics can be saved to the clipboard and exported to other applications. Multi-pane windows facilitate the viewing of statistics and VCC data, including graphs, tabular data and libraries. The Domino-360 ATM analyzer provides extensive statistics for SVC calls, physical layer performance and defect indicators such as alarms and errors counts, as well as ATM layer performance and defects such as cell counts, cell rates and HEC error counts. These statistics are vital for troubleshooting in an ATM environment.

Detailed Virtual Channel and QoS Statistics

The analyzer collects and displays extensive statistics on a per-VC basis. Included is the ability to auto-discover VPI/VCI's that are in use. In addition, extensive call setup information is maintained and displayed. This information includes agreed upon QoS, traffic contract and type of traffic. Individual views are provided for per-VC counts (cells, OAM, RM, CLP=1 cells

and AAL5 PDUs), rates (traffic tales with histories on selected quote-RMA from R.A.E. Services Inc.

VCs) and utilization (selected VCs and top users). The view is a selected view is a selected view is a selected view is a selected view in the application of the view is a selected view is a selected view in the application of the view is a selected view in the application of the view is a selected view in the view in the view is a selected view in the view in the view is a selected view in the view in the view in the view is a selected view in the view in the view in the view in the view is a selected view in the vie application also provides a call signaling history of SVC calls that simplifies monitoring and debugging in an ATM SVC environment.

QoS setups are facilitated by the QoS Setup Wizard, which specifies which measurements to perform and which VCC to measure. The analyzer provides both in-service and out-of-service QoS measurements. The in-service measurement consists of one-point cell delay variation (CDV) and cell misinsertion rate measurements. The out-of-service QoS measurements include a two-point CDV measurement, cell transfer delay measurement and distribution as well as providing more than the recommended ITU-T.356 and ATM Forum measurements. These measurements are in accordance with applicable ITU-T and ATM Forum recommendations and guidelines. Distribution and historical graphs of all the QoS measurements are invaluable for troubleshooting traffic content and SLA testing and verification.

In-service QoS measurements can be taken while monitoring live traffic because they do not affect network traffic. The drawback of in-service QoS measurements is that they are limited to a single measurement point. Out-of-service QoS measurements, on the other hand, are more accurate because measurements are taken at two different points on the network. Additionally, more QoS parameters can be measured when out-of-service QoS measurements are performed. Unlike in-service measurements, out-of-service QoS measurements can interrupt traffic and should not be performed while monitoring

easily exported for use in spreadsheets, word processors, and other desktop office applications.

Transportable Network Analysis Kit on Alert with Domino

Domino Attaché is a rugged aluminum case used for safe and secure transportation of Domino analyzers. Domino units are mounted in a small, expandable rack system in the base shell while an optional laptop computer can be secured on top of the rack system with a heavy-duty Velcro® strap. The removable case top allows full access for mounting, pre-wiring and plugging in Domino analyzers, providing an analysis kit ready for immediate us



on to Domino Attaché, WWG offers a soft carrying case with ulder strap. This bag has room for a Domino analyzer, laptop and Ses. Our single or multiple-card interface module containers also ake shipping and storage a breeze.

Notebook Compatibility and Connection

With its small footprint and stackable design, Domino accommodates many popular Windows-compatible notebook PCs. This compatibility allows users to choose and upgrade PCs according to needs and desires, so users are not held captive to a proprietary platform or yesterday's PC standard.

Warranty Information

Domino-360 ATM Internetworking Analyzers come with a two-year manufacturer's warranty, which covers any defects detected by the customer or Wavetek Wandel Goltermann. Domino-360 ATM software is covered under a 90 day manufacturer's warranty.

To receive a calibration and/or repair quote-RMA from R.A.E. Services Inc. Specifications Click here>> www.raeservices.com/services/quote.htm

General Specifications	Specifications
Safety	PMD to ITU G.703 for 2.048 Mbps network
IEC-1010-1, EN61010-1	Transmit timing recovered clock or internal clock
For laser applications 21CFR 1040.10/11, IEC-825, EN60825	Receive termination
(€ c(!) us	Framing ITU G.704 FAS or CRC4
Ambient Temperature Range	Line coding
Use	Cell mapping
Storage and transport20 to +60 °C	Transmission Convergence ATM Forum AF-PHY-0064
Dimensions	Interface-Specific Layer 1-2 Processes
Weight approx. 3.0 kg (6.5 lbs.)	Cell Delineation
Size (L×W×H) 290×283×70 mm (11.4×11.1×2.8 inches)	HEC Coset
	Cell Payload Scrambling On or Off
OC-3/STM-1 Interface Specifications	Addressing
Interface Connectors	Modes of Operation
Single mode ST connectors: Two receive, two transmit	Test modes:
Multi-mode SC connectors: Two receive, two transmit	
155 UTP Two receive, two transmit	Status LEDs for Each Receiver
Optical Specifications	
Single mode Bellcore TA-NWT-000253 and	Statistics (S)
	E1Line Frequency Frame Errors, FEBEs, CRC4, LCV,
intermediate reach 1310 nm Class 1 Laser product Multi-mode	defect sacond counts for LOS, LOF, AIS, RDI,
Framing	Loss of Cell Sync
Section scrambling Oct of of Stiller	PPCP LOF, Yellow, Frame/POI, FEBE, BIP8
	A.M (for each receiver) Assigned cell count, HEC errors,
Optical Power Specification	
Single mode	Narm/Error Insertion
Optical output power min. 15 dBm, nom. 10 dBm, max. 8 dBm	Continuous Physical Alarms AIS, RDI
Sensitivity min. 36 dBm Multi-mode	Continuous PLCP Alarms. Yellow
Optical output power min. 19 dBm, max. 4 dBm	Continuous PLCP Errors BIP8, Frame, FEBE, POI
Sensitivity	Contravous ATM Errors HEC Correctable
bensitivity	
Interface-Specific Layer 1-2 Processes	nterface Specifications
Transmission Convergence to ATM Forum INIX 0/3 1/4 0	Interface Specifications
Transmission Convergence to ATM Forum UNI 3 0/3.1/4 9 Cell Delineation	Interface Specifications Interface Connectors BNC
Transmission Convergence to ATM For m Wil 0/3.1/4 Cell Delineation	Interface Connectors BNC 2 receive, 2 transmit
Transmission Convergence to ATM Forum UNI 20/3.1/4 9 Cell Delineation HEC Coset. 55H or none Modes of Operation	Interface Connectors BNC 2 receive, 2 transmit Specifications
Transmission Convergence to ATM Forting INI 0/3.1/4 0 Cell Delineation	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD
Transmission Convergence to ATM Forum UNI 20/3.1/4 9 Cell Delineation HEC Coset. 55H or none Modes of Operation	Interface Connectors
Transmission Convergence to ATM Forum UNI 9 0/3.1/4 0 Cell Delineation HIC HEC Coset	Interface Connectors
Transmission Convergence to ATM Forum UNI 9.0/3.1/4 0 Cell Delineation HIC HEC Coset	
Transmission Convergence to ATM Forum UNI 9 0/3.1/4 0 Cell Delineation HIC HEC Coset	$\begin{array}{ c c c c c } \hline \textbf{Interface Connectors} \\ \hline BNC & 2 \text{ receive, 2 transmit} \\ \hline \textbf{Specifications} \\ \hline PMD & ANSI T1.107-1988/ITU G.703 \\ \hline Framing. & ITU G.832 \\ \hline Tx Timing & recovered clock or internal clock \\ \hline Receive Termination & 75\Omega \text{ or high impedance} \\ \hline Line Encoding & HDB3 \\ \hline \end{array}$
Transmission Convergence to ATM Forting UNI 0/3.1/4 (Cell Delineation	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes
Transmission Convergence to ATM Forting UNI 0/3.1/4 P Cell Delineation	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0034
Transmission Convergence to ATM Forting UNI 0/3.1/4 P Cell Delineation	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0034 Cell Delineation HEC or PLCP
Transmission Convergence to ATM Forting UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or none) Modes of Operation Test modes Monitor, Emplain Oser, or Emulate Natwork, UNI 3/0/3.1/4.0, PVC (Public Private) Status LEDs for Each Receiver Signal France Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line FERAS defect-second counts for Section Loss of France, Loss of Signal, Invalid clock, OOF, Line LOR/Line AIS, Line RDI, Path AIS,	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0034 Cell Delineation HEC or PLCP Cell Payload Scrambling On or Off
Transmission Convergence to ATM Forting UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or nome) Modes of Operation Test modes Monitor, Emplaire Server Emulate Natwork, UNI 3/0/3.1/4.0, PVC Public Private Status LEDs for Each Receiver Signal France Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line FESAs defect-second counts for Section Loss of France, Loss of Signal, Invalid clock, OOF, Line LOS/ Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync	Interface Connectors BNC 2 receive, 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0034 Cell Delineation HEC or PLCP
Transmission Convergence to ATM Forting UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or nome) Modes of Operation Test modes Monitor, Emplaire Near Femulate Natwork, UNI 30.3.1.4.0, PVC Public Private Status LEDs for Each Receiver Signal Frame Sync, Als, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line FEBAs (defect-second counts for Section Loss of Frame Loss of Signal, Invalid clock, OOF, Line LOP/Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors,	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
Transmission Convergence to ATM Fortin UNI 0/3.1/4 Cell Delineation HEC Coset. Modes of Operation Test modes Monitor, Embatic Deers of Emulate Natwork, UNI 3/0/3.1/4.0, PVC Public Private Status LEDs for Each Receiver Signal France Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Hine Frequency Path/Line/Section BIPs, Path/Line Frequency Description Signal, Invalid clock, OOF, Line LOP, Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate,	$ \begin{array}{ c c c c } \hline \textbf{Interface Connectors} \\ \hline BNC & 2 \text{ receive, 2 transmit} \\ \hline \textbf{Specifications} \\ \hline PMD & ANSI T1.107-1988/ITU G.703 \\ \hline Framing. & ITU G.832 \\ \hline Tx Timing & recovered clock or internal clock \\ \hline Receive Termination & 75\Omega \text{ or high impedance} \\ \hline Line Encoding & HDB3 \\ \hline \textbf{Interface-Specific Layer 1-2 Processes} \\ \hline Transmission Convergence. & ATM Forum AF-PHY-0034 \\ \hline Cell Delineation & HEC \text{ or PLCP} \\ \hline Cell Payload Scrambling & On \text{ or Off} \\ \hline Addressing & UNI \text{ or NNI} \\ \hline Cell Mapping. & Direct \\ \hline \textbf{Modes of Operation} \\ \hline \end{array} $
Transmission Convergence to ATM Forting UNI 0/3.1/4 P Cell Delineation HEC Coset	Interface Connectors 2 receive , 2 transmit Specifications PMD ANSI T1.107-1988/ITU G.703 Framing. ITU G.832 Tx Timing recovered clock or internal clock Receive Termination 75Ω or high impedance Line Encoding HDB3 Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0034 Cell Delineation HEC or PLCP Cell Payload Scrambling On or Off Addressing UNI or NNI Cell Mapping Direct Modes of Operation Test Modes Monitor, Emulate User or Emulate Network
Transmission Convergence to ATM Forting UNI 0/3.1/4 P Cell Delineation HEC Coset	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
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Transmission Convergence to ATM Forting UNI 0/3.1/4 P Cell Delineation HEC Cell Delineation HEC HEC Coset. 55H or none Modes of Operation Test modes Monitor, Explicit Descript Emulate Network, UNI 30, 31, 4.0, PVC, Public Private Status LEDs for Each Receiver Signar Frame Sync, ATS, cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line Frequency Path/Line/Section BIPs, OOF, Line LOP/Line AIS, Line RDI, Path AIS, ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate, filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion Single error insertion OC-3/STM-1 Line, Path, and Section BIP errors	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or none) Modes of Operation Test modes Monitor, Embatic Beer, or Emulate Natwork, UNI 30, 3.1, 4.0, PVC, Publis Private Status LEDs for Each Receiver Signal Frame Sync, AtS, Cell Sync, Slicing Statistics OC-3/STM-1 Line FEBIS defect-second counts for Section Loss of Frame Loss of Signal, Invalid clock, OOF, Line LOP Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate, filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion Single error insertion OC-3/STM-1 Line, Path, and Section BIP errors ATM Cell HEC (correctable) and HEC (uncorrectable)	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or none) Modes of Operation Test modes Monitor, Embatic Beer, or Emulate Natwork, UNI 30, 3.1, 4.0, PVC, Publis Private Status LEDs for Each Receiver Signal Frame Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line FEBIs defect-second counts for Section Loss of Frame Loss of Signal, Invalid clock, OOF, Line LOP Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate, filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion Single error insertion OC-3/STM-1 Line, Path, and Section BIP errors ATM Cell HEC (correctable) and HEC (uncorrectable) Continuous alarm insertion	Interface Connectors BNC
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Transmission Convergence to ATM Fortin UNI 0/3.1/4 (Cell Delineation HEC Coset. 55H or none) Modes of Operation Test modes Monitor, Embatic Beer, or Emulate Natwork, UNI 30, 3.1, 4.0, PVC, Publis Private Status LEDs for Each Receiver Signal Frame Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line FEBIs defect-second counts for Section Loss of Frame Loss of Signal, Invalid clock, OOF, Line LOP Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate, filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion Single error insertion OC-3/STM-1 Line, Path, and Section BIP errors ATM Cell HEC (correctable) and HEC (uncorrectable) Continuous alarm insertion	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 Cell Delineation HEC Coset. 55H or none Modes of Operation Test modes Monitor, Embatic Beer, or Emulate Natwork, UNI 3.0, 3.1,4.0, PVC Public Private Status LEDs for Each Receiver Signal France Sync, Ats, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line Frequency Path/Line RDI, Path AIS, OF, Line LOR Line AIS, Line RDI, Path AIS, Path RDI, Loss of Cell Sync ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion OC-3/STM-1 Line, Path, and Section BIP errors ATM Cell HEC (correctable) and HEC (uncorrectable) Continuous alarm insertion OC-3/STM-1 Path and Line RDI, Path and Line RDI, Path and Line AIS, and C2 Unequipped	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 P Cell Delineation HIC HEC Coset. 55H or point Modes of Operation Test modes Monitor, Extratal Secret Femulate Natwork, UNI 3/0, 21, 4.0, PVC, Pablin Private Status LEDs for Each Receiver Signal Frame Sync, AS, Cell Sync, Slicing Statistics OC-3/STM-1 Line Frequency Path/Line/Section BIPs, Path/Line Frequency Path/Line/Section BIPs, OOF, Line LOP Line AlS, Line RDI, Path AIS, OOF, Line LOP Line AIS, Line RDI, Path AIS, ATM (for each receiver) assigned cell count, HEC errors, assigned cell rate, utilization and payload bit rate, filtered cell count, filtered cell rate and payload bit rate Alarm/Error Insertion Single error insertion OC-3/STM-1 Line, Path, and Section BIP errors ATM Cell HEC (correctable) and HEC (uncorrectable) Continuous alarm insertion OC-3/STM-1 Path and Line AIS, and C2 Unequipped	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 P Cell Delineation HIC HEC Coset	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNIX 0/3.1/4 PCell Delineation HIC HEC Coset	Interface Connectors BNC
Transmission Convergence to ATM Fortin UNI 0/3.1/4 P Cell Delineation HIC HEC Coset	Interface Connectors BNC

Alarm/Error Insertion To receive a calibration and/or repair que Single Error Insertion Click here>> www.raeservices	ote _x RMA from R.A.E. Services lacovered clock or internal clock configuration. Com/services/quote.htm
E3 LCV Continuous Alarm Insertion	Line Encoding
E3	
Continuous Error Insertion	Interface-Specific Layer 1-2 Processes
E3 BIP-8, Frame, FEBE	Transmission Convergence ATM Forum AF-PHY-0016.000 Cell Delineation
ATM Cell	Cell Payload Scrambling On or Off
DS3 Interface Specifications	Addressing UNI or NNI Cell Mapping Direct, G.804
Interface Connectors	Modes of Operation
BNC 2 receive, 2 transmit	Test Modes Monitor, Emulate User or Emulate Network
Specifications	
PMD	Status LEDs for each Receiver
Framing ITU G.804, M23 or C-Parity Tx Timing recovered clock or internal clock	Statistics Cell Sync, Red, Yellow (RDI), AIS
Receive Termination	DS1 Line Frequency, befect second wants for Loss of Signal, Loss of Frame, AIS ROLLOSS of cell sync and Red,
Interface-Specific Layer 1-2 Processes Transmission Convergence ATM Forum AF-PHY-0054,	ATM
	Alarm/Erxo Insertion
Cell Delineation	Continuous Alarm Insertion DS1
Addressing	Continuous Error Inserton
Modes of Operation	ATM Cell HEC (correctable)
Test Modes Monitor, Emulate User or Emulate Network	ATM 25 M6ps Interface Specifications
	Interface Connectors
Status LEDs for Each Receiver	RJ- Ω ISO/IEC 603-7 to support UTP-MIC at 100Ω , 120Ω
Signal Present, AIS, Frame Syne, Sell Sync	EIA/TIA-568-A via adapter cable to support $EIA/TIA-568-A$ via adapter cable to support cable to support cab
Statistics DS3 Line Frequency, defect second counts for Loss of Signal, (Specifications
Loss of Frame, AIS, RDI, OOC, Idle and Red, Framing Errors, V	MD
PLCP defect second counts for LOF and Yellow	Monitor Type
Frame, FEBOTA -8	Tx Timing Crystal or Low Jitter Timing Reference
ATM (same as other interfaces)	Line Termination 100 Ω , 120 Ω (UTP Cat 3,4,5) or 150 Ω (STP)
Alarm/Error Insertion	Line Encoding
Single Error Insertion	Interface-Specific Layer 1-2 Processes
DS3 LCV	Transmission Convergence ATM Forum AF-PHY-0040.000
Continuous Alarm Insertion	Cell Delineation Start_of_Cell Command (X_X or X_4)
DS3	Cell Payload Scrambling On
PLCP Yellow	Addressing
Continuous Error Insection	Cell Mapping Direct
DS3	Modes of Operation
PLCP	Test Modes Monitor, Emulate User or Emulate Network
ATM Cell	
DS1 Interface Specifications	Status LEDs for Each Receiver
Interface Connectors	
RJ-48C 2 receive, 2 transmit	
Bantum Plug	Statistics
Specifications	
PMD	
Monitor Type Bridged or DSX	
Line Build Out selections from 0 to 665 ft, 0 to -22.5 dB	Alarm/Error Insertion
Rx Levels	

Domino-360 ATM Internetworking Analyzer

Domino-360 ATM Internetworking Analyzer includes BAM, appropriate LIM, LED overlay, integrated power supply, user documentation, and notebook/parallel		E3 ATM Pack	BN 9316/90.02
		DS3 ATM Pack	BN 9316/90.18
port cable	,	E1 ATM Pack	BN 9316/90.01
OC-3/STM-1 ATM Multi-Mode Pack	BN 9316/90.03	DS1 ATM Pack	BN 9316/90.17
OC-3/STM-1 ATM		25 Mbps ATM Pack	BN 9316/90.64
Single Mode Pack	BN 9316/90.04	DominoNAS	/
OC-3/STM-1 UTP Pack	BN 9316/90.05	(includes ATM software)	BN 9316/93.30

Acterna Sales Offices

North America

PO Box 13585 Research Triangle Park, NC

Tel. +1 919 941-5730 Fax 1 919 941-5751

entral & East Europe

Hostfach 13 Eisabethstrasse 36 A-2500 Baden

Tel. +43 2252 85521 0 Fax +43 2252 80727

Latin America

Av. Eng. Luis Carlos Berrini, 936-8/9. andar 04571-000 Sao Paulo, SP Brazil

Tel. +55 11 5503 3800 Fax +55 11 5505 1598

CIS Countries

1st Neopalimovskiy per. 15/7 (4th floor) 119121 Moscow Russia

Tel: +7 095 248 2508 Fax +7 095 248 4189

Asia-Pacific

PO Box 141 South Melbourne, Victoria 3205

Australia

Tel. +61 3 9690 6700 Fax +61 3 9690 6750

West Europe

Arbachtalstrasse 6 D-72800 Eningen u.A. Germany

Tel. +49 7121 86 2222 Fax +49 7121 86 1222

Internet Address

www.acterna.com

